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| APPLICATION NO. | F | FILING DATE | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO. | CONFIRMATION NO. | |
|---|------------|-------------|----------------------|---------------------|------------------|--|
| 10/713,054 | 11/17/2003 | | Masanobu Ogino | 245557US0S X | 1158 | |
| 22850 | 7590 | 10/30/2006 | | EXAN | EXAMINER | |
| C. IRVIN MCCLELLAND | | | | NGUYEN, THANH T | | |
| OBLON, SPIVAK, MCCLELLAND, MAIER & NEUSTADT, P.C. 1940 DUKE STREET | | | ART UNIT | PAPER NUMBER | | |
| ALEXANDRIA, VA 22314 | | | | 2813 | | |

DATE MAILED: 10/30/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

| | Application No. | Applicant(s) | | | | | |
|---|---|---|--|--|--|--|--|
| Office Action Summany | 10/713,054 | OGINO ET AL. | | | | | |
| Office Action Summary | Examiner | Art Unit | | | | | |
| The MAILING DATE of this second and second | Thanh T. Nguyen | 2813 | | | | | |
| The MAILING DATE of this communication ap Period for Reply | pears on the cover sheet with the | correspondence address | | | | | |
| A SHORTENED STATUTORY PERIOD FOR REPL WHICHEVER IS LONGER, FROM THE MAILING D - Extensions of time may be available under the provisions of 37 CFR 1. after SIX (6) MONTHS from the mailing date of this communication. - If NO period for reply is specified above, the maximum statutory period - Failure to reply within the set or extended period for reply will, by statut Any reply received by the Office later than three months after the mailin earned patent term adjustment. See 37 CFR 1.704(b). | DATE OF THIS COMMUNICATION 136(a). In no event, however, may a reply be a will apply and will expire SIX (6) MONTHS from the cause the application to become ABANDON | DN. timely filed m the mailing date of this communication. JED (35 U.S.C. § 133). | | | | | |
| Status | | | | | | | |
| 1) Responsive to communication(s) filed on 25 A | Nugust 2006. | | | | | | |
| _ | <u> </u> | | | | | | |
| 3) Since this application is in condition for allowa | | | | | | | |
| closed in accordance with the practice under | Ex parte Quayle, 1935 C.D. 11, 4 | 453 O.G. 213. | | | | | |
| Disposition of Claims | | | | | | | |
| 4)⊠ Claim(s) <u>1-17</u> is/are pending in the application | 1. | | | | | | |
| 4a) Of the above claim(s) 6-9, 14-17 is/are with | | | | | | | |
| 5) Claim(s) is/are allowed. | | | | | | | |
| 6) Claim(s) <u>1-5 and 10-13</u> is/are rejected. | | | | | | | |
| 7) Claim(s) is/are objected to. | | | | | | | |
| 8) Claim(s) are subject to restriction and/o | or election requirement. | | | | | | |
| Application Papers | | | | | | | |
| 9) The specification is objected to by the Examine | er. | | | | | | |
| 10) The drawing(s) filed on is/are: a) acc | | Examiner. | | | | | |
| Applicant may not request that any objection to the | | | | | | | |
| Replacement drawing sheet(s) including the correct | | | | | | | |
| 11)☐ The oath or declaration is objected to by the E | xaminer. Note the attached Offic | e Action or form PTO-152. | | | | | |
| Priority under 35 U.S.C. § 119 | | | | | | | |
| 12) Acknowledgment is made of a claim for foreign a) All b) Some * c) None of: 1. Certified copies of the priority document 2. Certified copies of the priority document 3. Copies of the certified copies of the priority document application from the International Burea | ts have been received. ts have been received in Applica prity documents have been received (PCT Rule 17.2(a)). | ntion No ved in this National Stage | | | | | |
| * See the attached detailed Office action for a list | of the certified copies not receiv | /ed. | | | | | |
| Attachment(s) | | | | | | | |
| 1) Notice of References Cited (PTO-892) | 4) 🔲 Interview Summar | | | | | | |
| Notice of Draftsperson's Patent Drawing Review (PTO-948) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date 6/14/06 | Paper No(s)/Mail I 5) Notice of Informal 6) Other: | Date Patent Application (PTO-152) | | | | | |

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DETAILED ACTION

Request for Continued Examination

The request filed on 8/2/06 for a Request for Continued Examination (RCE) under 37 CFR 1.114 is acceptable and an RCE has been established. An action on the RCE follows.

Information Disclosure Statement

The IDS filed on 6/14/06 has been considered.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1 is rejected under 35 U.S.C. 102(b) as being anticipated by Menegoli (U.S. Patent No. 6,133,107).

Referring to figures 12-14, Menegoli a semiconductor substrate comprising:

a lightly doped substrate (50, P-) that contains impurities at a low concentration (see figure 12, col. 4, lines 66-67);

a heavily doped diffusion layer (54, N+, see figure 12, col. 5, lines 3-10) which entirely covers a top of the lightly doped substrate (50) and is higher impurity concentration than the lightly doped substrate (see col. 4, lines 66-67, col. 1-10); and

an epitaxial layer (60, col. 5, lines 22-26) which entirely covers a top of the heavily doped diffusion layer and contains impurities at a lower concentration than the heavily doped diffusion layer (see figure 12, col. 5, lines 22-26).

Claims 10, 11, 13 are stand rejected under 35 U.S.C. 102(b) as being anticipated by Adamic Jr. (U.S. Patent No. 6,124,179), as previously applied.

Referring to figures 2a-2d, Adamic Jr., teaches a semiconductor substrate comprising: a heavily doped diffusion layer (N+)which entirely covers a top of a lightly doped substrate and is higher in impurity concentration than the lightly doped substrate, the lightly doped substrate being removed at a final stage of a process; and

an epitaxial layer (N-) which entirely covers a top of the heavily doped diffusion layer (N+) and contains impurities at a lower concentration than the heavily doped diffusion layer, wherein an impurity diffusion layer for forming a semiconductor device is formed the epitaxial layer (see figure 2a-2d, col. 8, lines 1-10, col. 11, line 33-40).

regarding to claim 11, wherein a resistance of the epitaxial layer $10\Omega cm$ or less (see col. 8, lines 7-10).

Regarding to claims 13, the lightly doped substrate and the heavily doped diffusions layer (N+) are of a first conductivity type, and the epitaxial layer is of a second conductivity type (232)

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With regard to claim 10, 13, the term "the lightly doped substrate being removed at a final stage of a process" is method recitations in a device claimed, and they are non-limiting, because only the final product is relevant, not the method of making. A product by process claim is directed to the product per se, no matter how actually made. See also MPEP 2113. Moreover, an old or obvious product produced by a new method is not a patentable product, whether claimed in "product by process" claims or not.

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims 2-5, 12 are rejected under 35 U.S.C. 103(a) as being unpatentable over Menegoli (U.S. Patent No. 6,133,107) as applied to claim 1 above, or Adamic Jr. (U.S. Patent No. 6,124,179) as applied to claims 10-11, 13 above in view of the Admitted Prior Art of the Present Invention, pages 1-4 and further in view of Werner (U.S. Patent No. 6,469,365).

Menegoli teaches a semiconductor substrate having a lightly doped (p-type), heavily doped and an epitaxialy layer, wherein the heavily doped diffusion layer and the epitaxial layer are of the same conductivity type (see figure 12-14, wherein both heavily doped and epitaxial layer are p-type). However, the reference does not teach the light doped substrate contains phosphorus or boron, the resistance of the epitaxial layer is 10Ω cm or less, and the lightly doped

layer is of a second conductivity type.

substrate and the heavily doped diffusion layer are of a first conductivity type, and the epitaxial

The Admitted prior art teaches the lightly doped substrate contains phosphorus or boron (see page 1, lines 20-25, meeting claim 2), the resistance of the epitaxial layer is 10Ωcm or less (see page 4, lines 12-13, meeting claim 3).

Therefore, it would have been obvious to a person of ordinary skill in the requisite art at the time of the invention was made would form a device having the light doped substrate contains phosphorus or boron, the resistance of the epitaxial layer is 10Ω cm or less in process of Menegoli or Adamic, Jr. as taught by the Admitted Prior because doping the material into the layer to improve the conductivity of the device.

Werner teaches forming a lightly doped substrate (p-), the heavily doped diffusion layer (p+), and the epitaxial layer (P) is of the same conductivity type (see figure 1, meeting claim 4).

Therefore, it would have been obvious to a person of ordinary skill in the requisite art at the time of the invention was made would form forming a lightly doped substrate (p-), the heavily doped diffusion layer (p+), and the epitaxial layer (P) is of the same conductivity type in process of Menegoli as taught by Werner because changing the conductivity type would provide a desire device.

It is known in the art to have the lightly doped substrate and the heavily doped diffusion layer are of a first conductivity type, and the epitaxial layer is of a second conductivity type.

Therefore, it would have been obvious to a person of ordinary skill in the requisite art at the time of the invention was made would form the lightly doped substrate and the heavily doped diffusion layer are of a first conductivity type, and the epitaxial layer is of a second conductivity

type in process of Menegoli because changing the conductivity type would provide a desire device.

Response to Arguments

Applicant's arguments filed 8/25/06 have been fully considered but they are not persuasive.

Applicant contends that Adamic Jr. does not teach the limitation of "the lightly doped substrate being removed at a final stage of a process". In response to applicant, this is a device claim, the lightly doped substrate is removed at the final structure. The method recitations in a device claimed, and they are non-limiting, because only the final product is relevant, not the method of making.

The additional references cited in form PTO-892 show further analogous circuitry. Specifically references (Werner, 6469365; Lee, 2002/0063266) are particularly relevant to claimed device and manufacture which recited in claims 1-5, 10-13. These references are deemed relevant and should be carefully reviewed before any amendment is filed.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Thanh Nguyen whose telephone number is (571) 272-1695, or by Email via address Thanh.Nguyen@uspto.gov. The examiner can normally be reached on Monday-Thursday from 6:00AM to 3:30PM.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Carl Whitehead, Jr., can be reached on (571) 272-1702. The fax phone number for this Group is (571) 273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pairdirect.uspto.gov. Should you have questions on access to thy Private PAIR system, contact the Electronic Business center (EBC) at 866-217-9197 (toll-free).

Thanh Nguyen

Patent Examiner

Patent Examining Group 2800

TTN